

#7

<b>LIST OF REFERENCES CITED BY APPLICANT</b> <i>(Use several sheets if necessary)</i>					ATTY. DOCKET NO. 9818-061-999		APPLICATION NO. To be assigned	
					APPLICANT Chang et al.			
					FILING DATE December 14, 2001		CIRCUIT NO. To be assigned	
<b>U.S. PATENT DOCUMENTS</b>								
EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	TRANSLATION DATE BY DATE	
KAN	AA	6,163,166	12/19/00	Bielby et al.	326	38		
KAN	AB	5,248,907	09/28/93	Lin et al.	326	33		
	AC							
	AD							
	AE							
	AF							
	AG							
	AH							
	AI							
<b>FOREIGN PATENT DOCUMENTS</b>								
		DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION YES NO	
	AJ							
	AK							
	AL							
<b>OTHER REFERENCES (Including Author, Title, Date, Pertinent Pages, Etc.)</b>								
KAN	AM	Kuang, J.B., et al., "PD/SOI CMOS Schmitt Trigger Circuits with Controllable Hysteresis," IBM Corporation, (April 2001)						
KAN	AN	"CD4016BM/CD40106BC Hex Schmitt Trigger," National Semiconductor, (February 1988)						
KAN	AO	"CMOS Schmitt Trigger—A Uniquely Versatile Design Component," Fairchild Semiconductor Application Note 140 (June 1975)						
EXAMINER <b>WELLS</b>				DATE CONSIDERED <b>6/29/02</b>				
*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.								